



11-23-04

RCE #
2283 ITW**REQUEST
FOR
CONTINUED EXAMINATION (RCE)
TRANSMITTAL**Address to:
Commissioner for Patents
Box RCE
Washington, DC 20231

Filing Date	08/02/01
Application Number	09/921377
First Named Inventor	G. Michael Uhler
Group Art Unit	2183
Examiner Name	Eddie Chan
Attorney Docket Number	MIPS.0110-00-US

This is a request for continued Examination (RCE) under CFR 1.114 of the above-identified application.

Request for Continued Examination (RCE) practice under 37 CFR 1.114 does not apply to any utility or plant application filed prior to June 8, 1995 or to any design application. See instruction Sheet for RCEs (not to be submitted to the USPTO) on page 2.

1. **Submission required under 37 CFR 1.1114**

- a. ☐ Previously submitted
- i. ☐ Consider the amendment(s)/reply under 37 CFR 1.1116 previously filed on _____
(Any unentered amendment(s) referred to above will be entered)
- ii. ☐ Consider the arguments in the Appeal Brief previously filed on _____
- iii. ☐ Other _____
- b. ☒ Enclosed
- i. ☐ Amendment
- ii. ☐ Affidavit(s)/Declaration
- iii. ☒ Information Disclosure Statement (IDS)
- iv. ☐ Other _____

2. **Miscellaneous**

- a. ☐ Suspension of action on the above-identified application is requested under 37 CFR 1.103C for a period of _____ months. (Period of suspension shall not exceed 3 months; Fee under 37 VFR 1.17(i) required)
- b. ☐ Other _____

3. **Fees** The RCE fee under 37 CFR 1.17 (e) is required by 37 CFR 1.114 when the RCE is filed.

- a. ☐ The Director is hereby authorized to charge the following fees, or credit any overpayments, to Deposit Account No. _____
- i. ☐ RCE fee required under 37 CFR 1.17(e)
- ii. ☐ Extension of time fee (37 CFR 1.136 and 1.17)
- iii. ☐ Other _____
- b. ☒ Check in the amount of \$ 790.00 enclosed
- c. ☐ Payment by credit card (Form PTO-2038 enclosed)

WARNING: Information on this form may be public. Credit card information should not be included on the form. Provide credit card information and authorization on PTO-2038.**SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT REQUIRED**

Name (Print/Type)	James W. Huffman	Registration No. (Attorney/Agent)	35,549	Telephone	(719) 475-7103
Signature				Date	11-21-04

CERTIFICATE OF MAILING OR TRANSMISSION

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as Express mail in an envelope addressed to: Commissioner for Patents, Box RCE, Washington, DC, 20231, or facsimile transmitted to the U.S. Patent and Trademark Office on the date shown below.

Name (Print/Type)	VICKI J. HOGAN		
Signature		Date	11-22-04

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:	G. Michael Uhler	Examiner:	Eddie Chan
Docket:	MIPS.0110-00-US	Art Unit:	2183
Customer No.:	23669		
Serial No.:	09/921377		
Filing Date:	08/02/01		
Title:	READ-ONLY ACCESS TO CPO REGISTERS		

STATEMENT OF RECOLLECTION

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

I declare that:

My name is Michael G. Uhler. I am the Chief Technology Officer at MIPS Technologies, Inc.

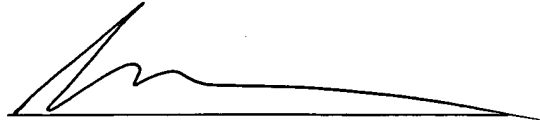
I am the sole inventor of U.S. Application No. 09/921400 entitled "ATOMIC UPDATE OF CPO STATE", and U.S. Application No. 09/921377 entitled "READ-ONLY ACCESS to CPO REGISTERS". Both of these applications have been assigned to MIPS Technologies, Inc.

It is my recollection that prior to the conception date of the above application, I saw a specification that included the instructions EI and DI which would have been used to enable and disable interrupts by setting/clearing a bit in a configuration register. I do not recall the source or title of the specification, and have been unable to locate a copy of the specification. Examples of instructions similar to my recollection are shown in the Alpha Architecture Handbook, and in the CompactRISC CR16A Programmer's Reference Manual, both of which are being submitted via an Information Disclosure Statement along with this Statement of Recollection. It is my recollection that the examples of the EI and DI instructions were used within a privileged or kernel mode of the processors.

I have searched my records at MIPS Technologies, Inc., as well as information on web sites from NEC, Toshiba, LSI and Philips for any information I could find that relates to the specification mentioned above. I have not found any documents in my search which relate to my recollection.

I am submitting this Declaration in accordance with my duty of disclosure under 37 CFR 1.56.

Respectfully submitted,



G. Michael Uhler
Chief Technology Officer
MIPS Technologies, Inc.

18 Nov 2004
Date

EXPRESS MAIL # EO 003235019US
11-22-04